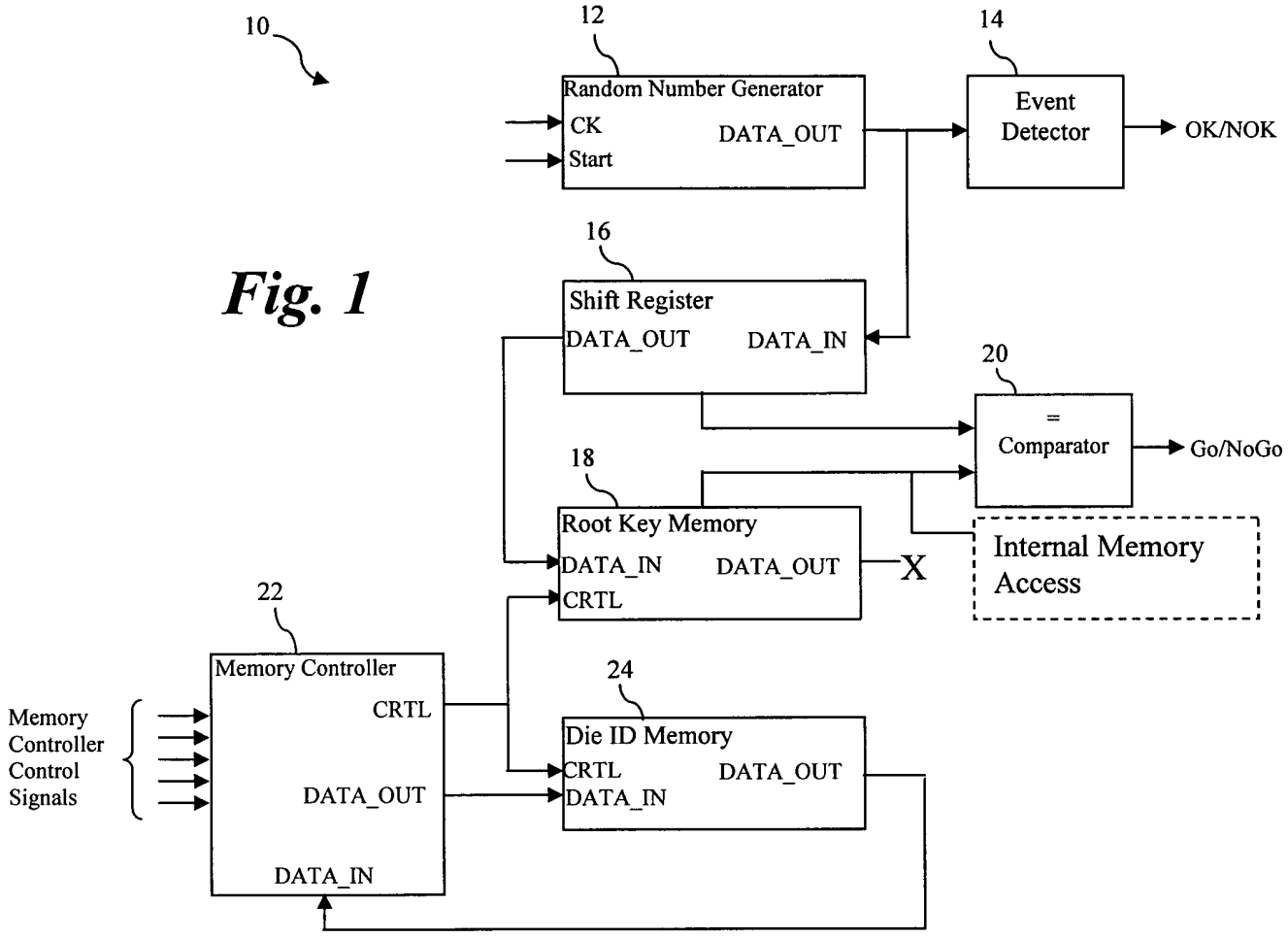
10
12
Random Number Generator
CK
Start
DATA_OUT
14
Event Detector
OK/NOK
16
Shift Register
DATA_OUT
DATA_IN
20
=
Comparator
Go/NoGo
18
Root Key Memory
DATA_IN
DATA_OUT
CRTL
X
Internal Memory Access
22
Memory Controller
CRTL
DATA_OUT
DATA_IN
Memory Controller Control Signals
24
Die ID Memory
CRTL
DATA_IN
DATA_OUT

Fig. 1

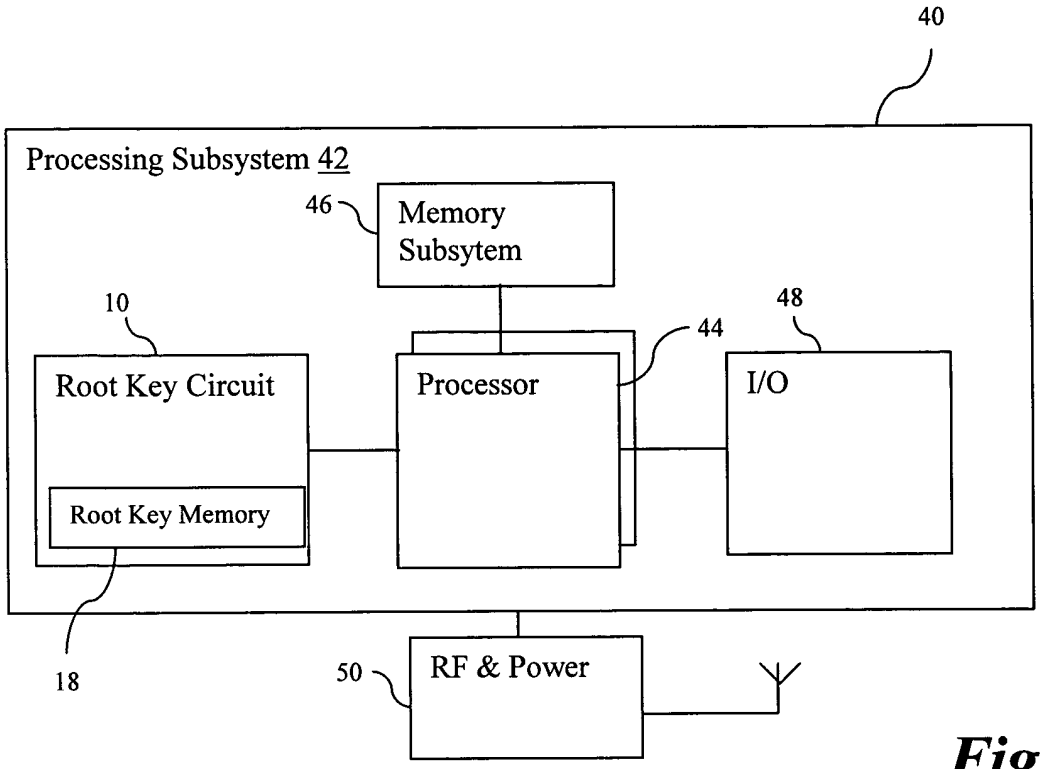
40
Processing Subsystem 42
46
Memory
Subsytem
10
48
44
Root Key Circuit
Processor
I/O
Root Key Memory
18
50
RF & Power

Fig. 2